Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSIII)

2SK2606

DC-DC Converter, Relay Drive and Motor Drive Applications

• Low drain-source ON resistance : RDS (ON) = 1.0 Ω (typ.)

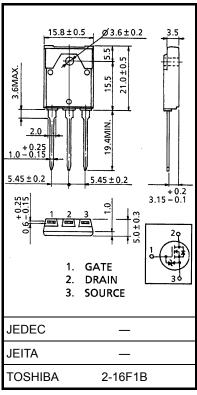
• High forward transfer admittance : $|Y_{fs}| = 7.0 \text{ S (typ.)}$

• Low leakage current $: I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 640 \text{ V)}$

• Enhancement mode : $V_{th} = 2.0 \sim 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	800	V	
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	800	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	8	Α	
Drain current	Pulse (Note 1)	I _{DP}	24	Α	
Drain power dissipation	r (Tc = 25°C)	P _D	85	W	
Single pulse avalanche	e energy (Note 2)	E _{AS}	883	mJ	
Avalanche current		I _{AR}	8	Α	
Repetitive avalanche e	nergy (Note 3)	E _{AR}	8.5	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ra	ange	T _{stg}	-55~150	°C	



Weight: 5.8 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.47	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	41.6	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 25.0 mH, $I_{AR} = 8 \text{ A}$, $R_G = 25 \Omega$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



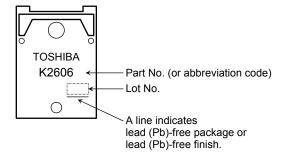
Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I _{GSS}	V _{GS} = ±30 V, V _{DS} = 0 V	_	_	±10	μA
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 640 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	800		_	V
Gate threshold v	oltage/	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0		4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 4 A,		1.0	1.2	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 15 V, I _D = 4 A	3.0	7.0	_	S
Input capacitano	e	C _{iss}			2160	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	45	_	
Output capacitance		Coss		_	200	_	
Switching time	Rise time	t _r	$V_{GS} = \frac{10V}{V_{GS}} = \frac{I_D = 4.0A}{V_{Out}} = \frac{V_{Out}}{V_{Out}}$ $V_{DD} = 400V$ $V_{DD} = 400V$ $V_{DD} = 400V$	_	25	_	- ns
	Turn-on time	t _{on}		ı	60		
	Fall time	t _f		ı	25		
	Turn-off time	t _{off}			110	_	
Total gate charge (gate-source plus gate-drain)		Qg			68		
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 8 \text{ A}$		38	_	nC
Gate-drain ("miller") Charge		Q _{gd}			30	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	8	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	24	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 8 A, V _{GS} = 0 V	_	_	-1.9	V
Reverse recovery time	t _{rr}	IDR = 8 A, V _{GS} = 0 V, dIDR / dt = 100 A / µs	_	1500	_	ns
Reverse recovery charge	Q _{rr}	1DR - 6 A, VGS - 6 V, diDR / dt - 100 A / μs	_	19	_	μC

Marking



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20070701-EN

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